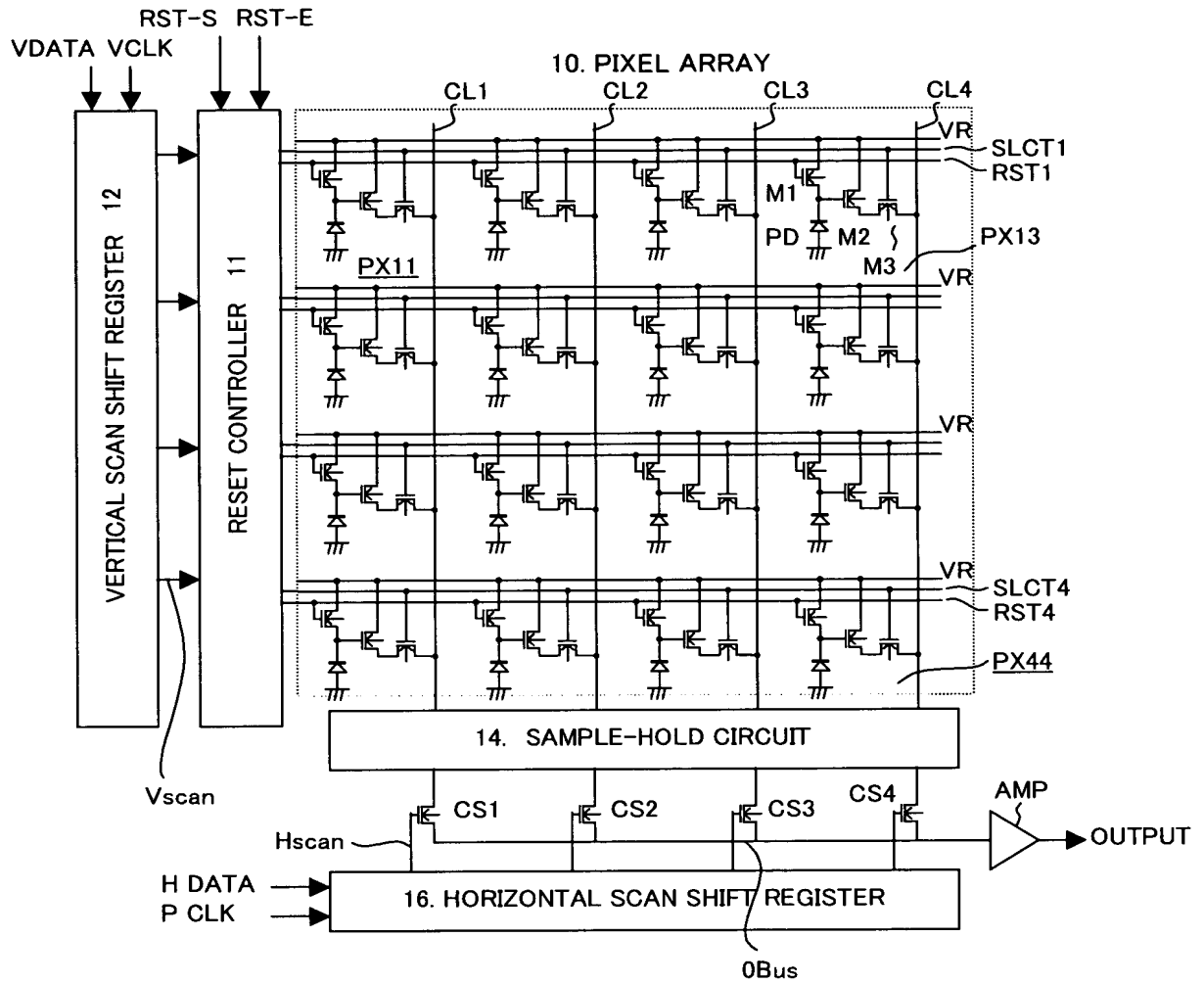
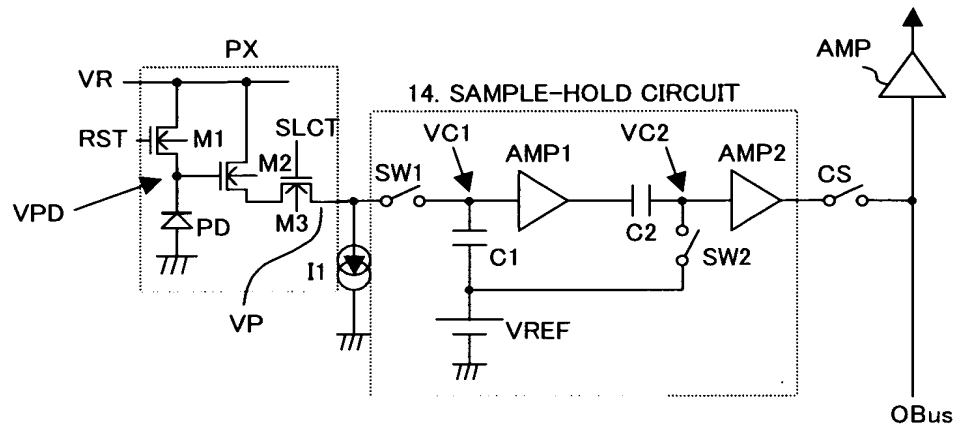


FIG. 1

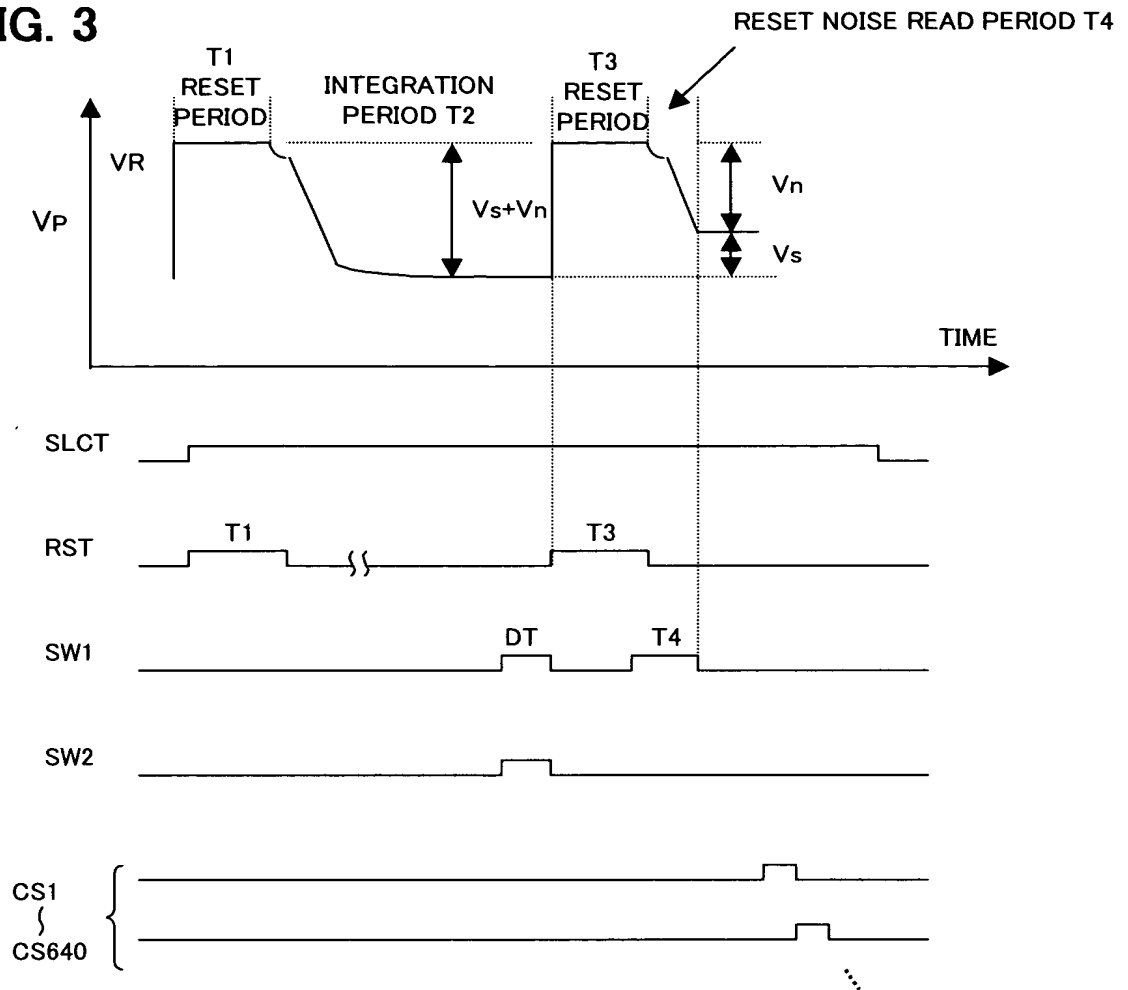


**FIG. 2**

**PRIOR ART**



**FIG. 3**



## 14. SAMPLE-HOLD CIRCUIT

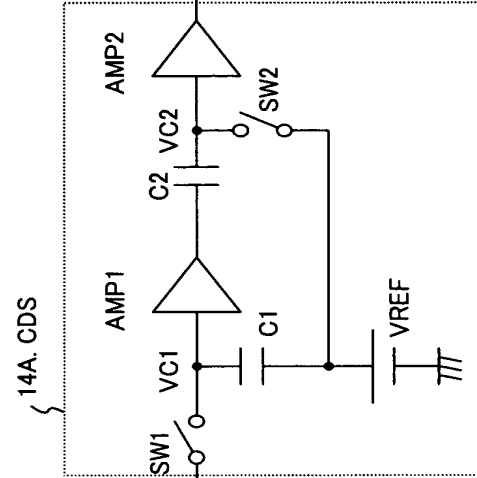
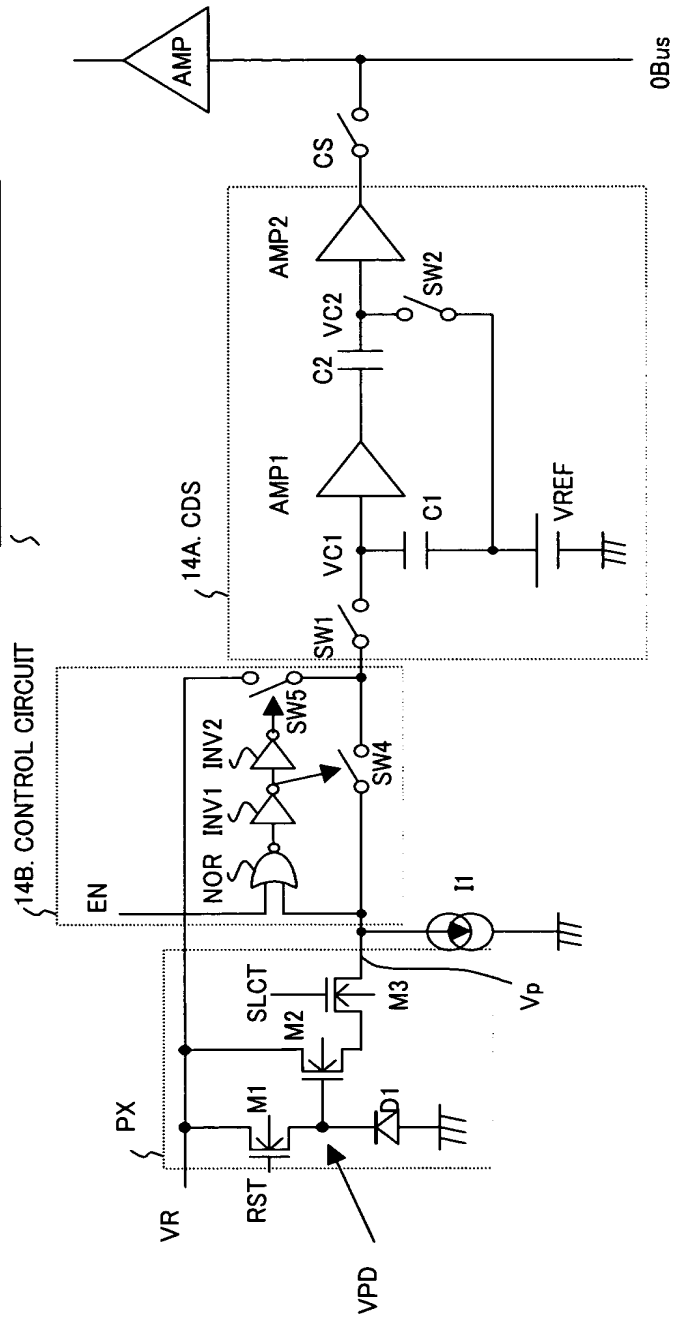


FIG. 5

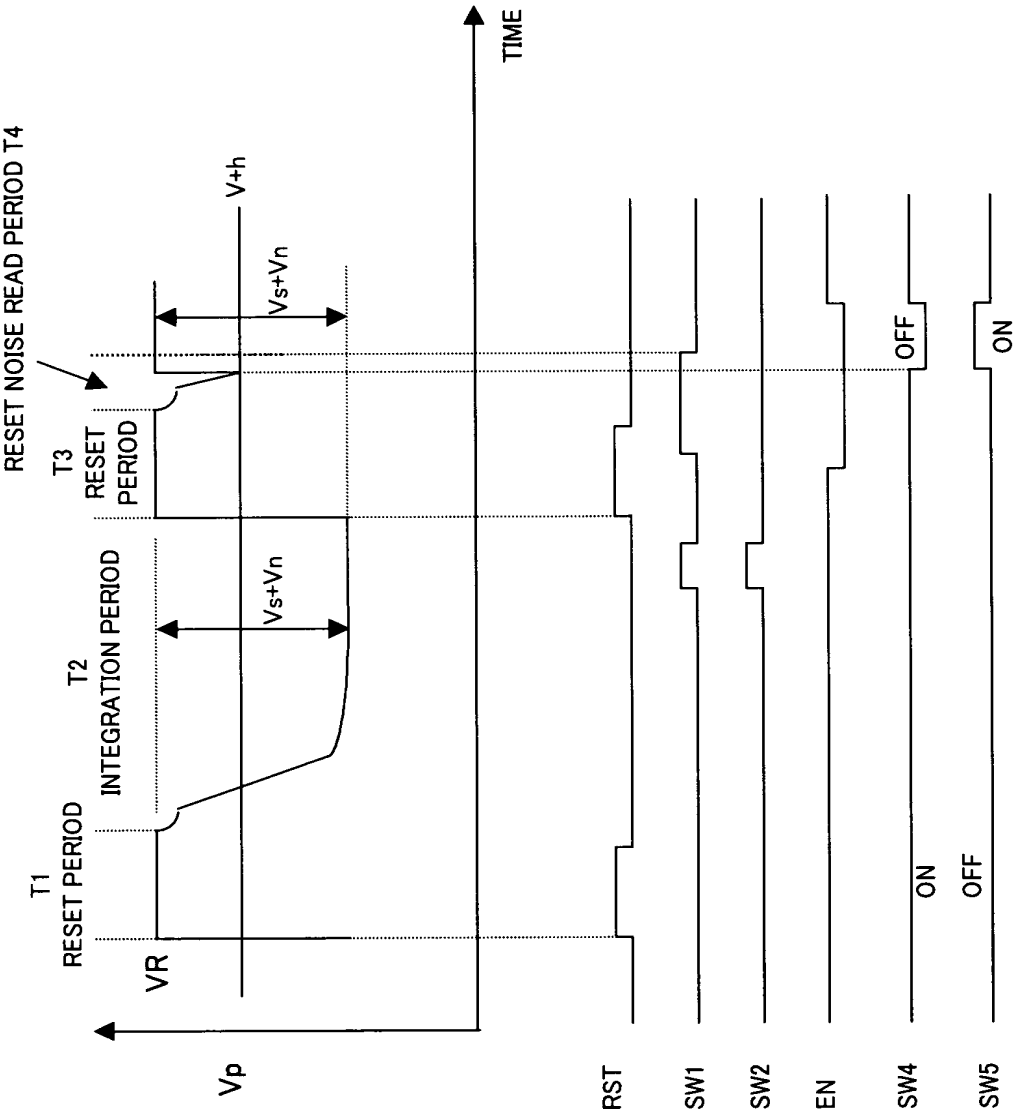


FIG. 6

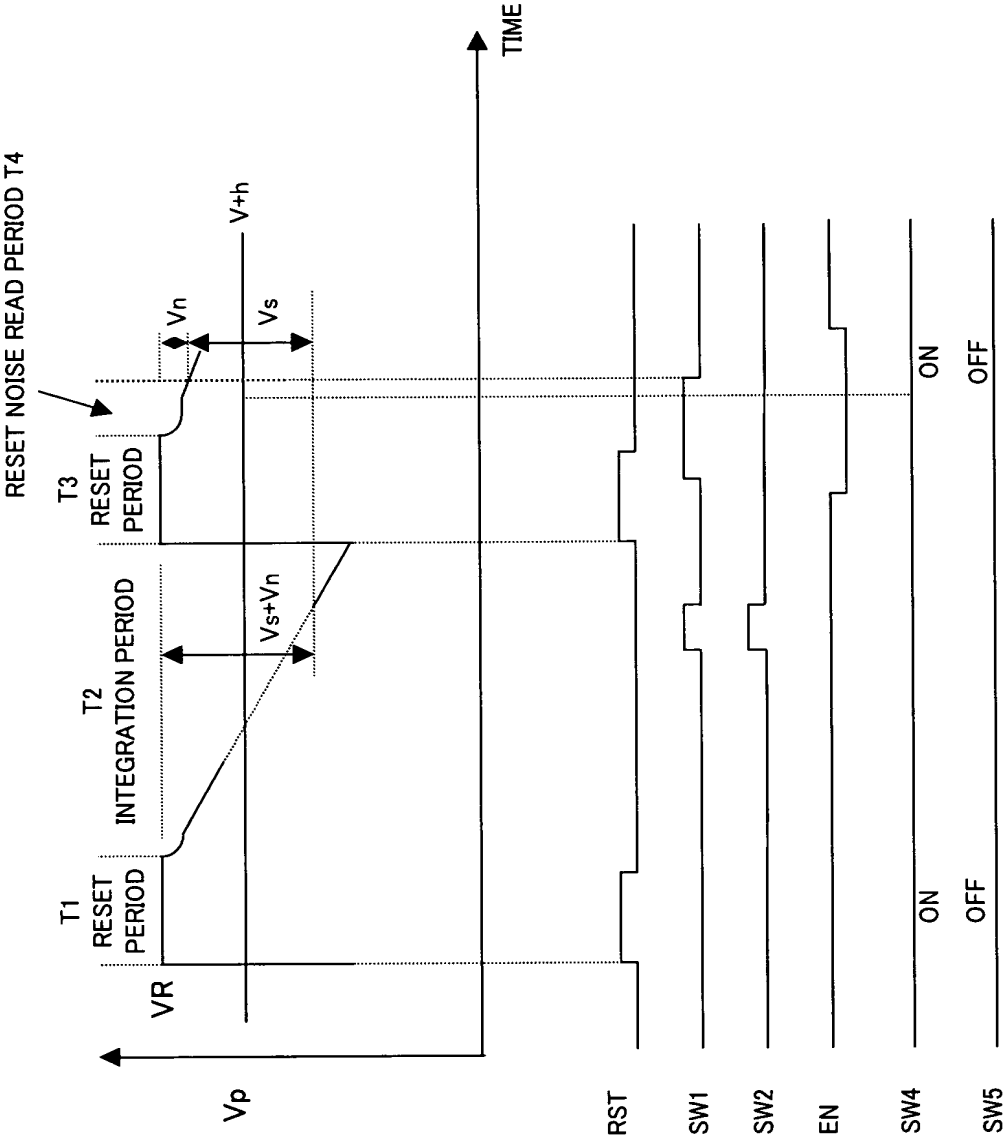


FIG. 7

